

09/838610  
04/19/01

ISSUE CLASSIFICATION	
Class	Subclass

**PATENT NUMBER**

**U.S. UTILITY Patent Application**

<div data-bbox="584 302 773 310"> <p><b>MT</b></p> <p><b>O.I.P.E.</b></p> <p><b>SCANNED</b></p> <p><b>TOY</b></p> <p><b>Q.A.</b></p> </div>	<div data-bbox="773 302 1000 310"> <p><b>PATENT DATE</b></p> </div>
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APPLICATION NO. 09/838610	SENT/PRIOR	CLASS 7.14	SUBCLASS 784	ART UNIT 2133	EXAMINER <i>Secadey</i>
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## APPLICANTS

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Norman Swenson  
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# TITLE

Code-programmable field-programmable architecturally-systolic Reed-Solomon BCH error correction decoder integrated circuit and error correction decoding method

PTO-2040  
12/89

**ISSUING CLASSIFICATION**

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION							

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b> Sheets Drwg.      Figs. Drwg.      Print Fig.		<b>CLAIMS ALLOWED</b>	
			Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	_____ (Assistant Examiner)      (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
			<b>ISSUE FEE</b>	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Primary Examiner)      (Date)		Amount Due	Date Paid
			<b>ISSUE BATCH NUMBER</b>	
	_____ (Legal Instruments Examiner)      (Date)			

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